File   View   Edit   Tools   Window   Help

L14: (2386) gate near2 extension
L15: (5718677) spacer lin$4
L16: (527) 14 with 15
L17: (23) 6 and 16
L18: (5059) 10 near 11
L19: (2375494) parallel
L20: (1823039) substrate
L21: (59421) 19 near6 20
L22: (12447) 4 near6 21
L23: (170) 6 same 22
L24: (80) "28" and 23
L25: (7) 18 and 23
L26: (10) 12 same 23
L27: (11) 25 26
L28: (353652) spacer
L29: (5584297) lin$4
L30: (26505) 28 near9 29
L31: (94) 30 same 8
L32: (26505) 30 same 15
L33: (28) 30 same 18
L35: (18) 21 and 34
L34: (122) 31 33
L36: (1) 34 and 14
L37: (2650) (257/301,532).CCLS.
L38: (4) 34 and 37
Failed
Saved

Browse   Queue   Clear

DBs: USPAT; US PGPUB; EPO; JPO; DERWENT; IBM_TDB   ☑ Plurals

Default operator   OR   ☐ Highlight all hit terms initially

21 and 34

Sept 2004

#	1	Inventor	Document I	Issue	P	Title	Current	Current XR	Retrieval	S	C	P		+			Image Doc.	P
1	☐ ☐	Fricke, Peter	US 2004015	20040	2	Memory array	365/177			☑	☐	☐	☐	☐	☐	☐	US 200401	☐
2	☐ ☐	Sell, Bernhar	US 2004014	20040	2	Method for fabricating a storage capacitor	438/243	438/253		☑	☐	☐	☐	☐	☐	☐	US 200401	☐
3	☐ ☐	Ikeda, Masah	US 2004009	20040	6	Liquid crystal display device and method of	349/130			☑	☐	☐	☐	☐	☐	☐	US 200400	☐
4	☐ ☐	Fricke, Peter	US 2004004	20040	2	Cubic memory array	365/222			☑	☐	☐	☐	☐	☐	☐	US 200400	☐
5	☐ ☐	Kim, Jin-Woo	US 2003018	20031	2	Nonvolatile memory cells having split gate st	365/200	257/E21.69		☑	☐	☐	☐	☐	☐	☐	US 200301	☐
6	☐ ☐	Fricke, Peter	US 2003018	20031	2	CUBIC MEMORY ARRAY	365/170			☑	☐	☐	☐	☐	☐	☐	US 200301	☐
7	☐ ☐	Fricke, Peter	US 6781858	20040	2	Cubic memory array	365/51	365/63		☑	☐	☐	☐	☐	☐	☐	US 678185	☐
8	☐ ☐	Kim, Jin-Woo	US 6753571	20040	2	Nonvolatile memory cells having split gate st	257/315	257/314		☑	☐	☐	☐	☐	☐	☐	US 675357	☐
9	☐ ☐	Ikeda, Masah	US 6671025	20031	5	Liquid crystal display device and method of	349/156	349/106;		☑	☐	☐	☐	☐	☐	☐	US 667102	☐
10	☐ ☐	Fricke, Peter	US 6643159	20031	2	Cubic memory array	365/51	365/130;		☑	☐	☐	☐	☐	☐	☐	US 664315	☐

Hit   Details   HTML

Ready   NUM